

FIG. 1

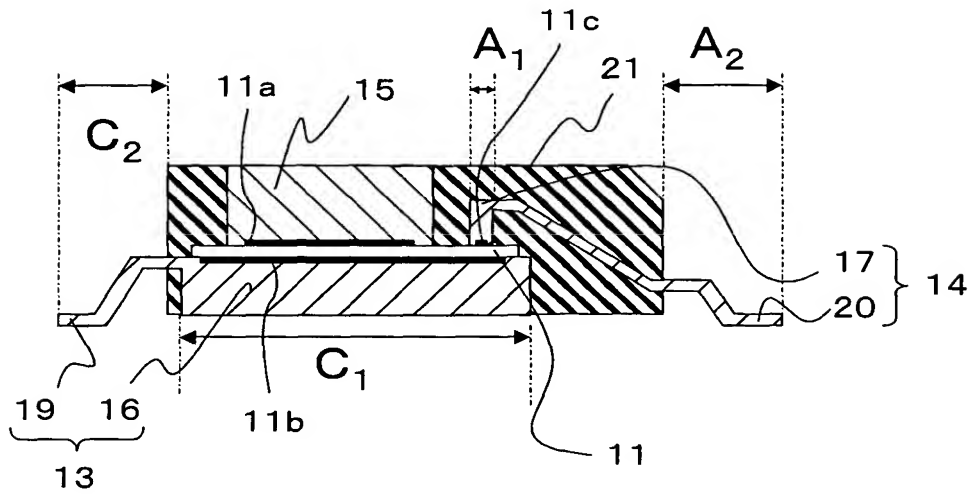


FIG. 2

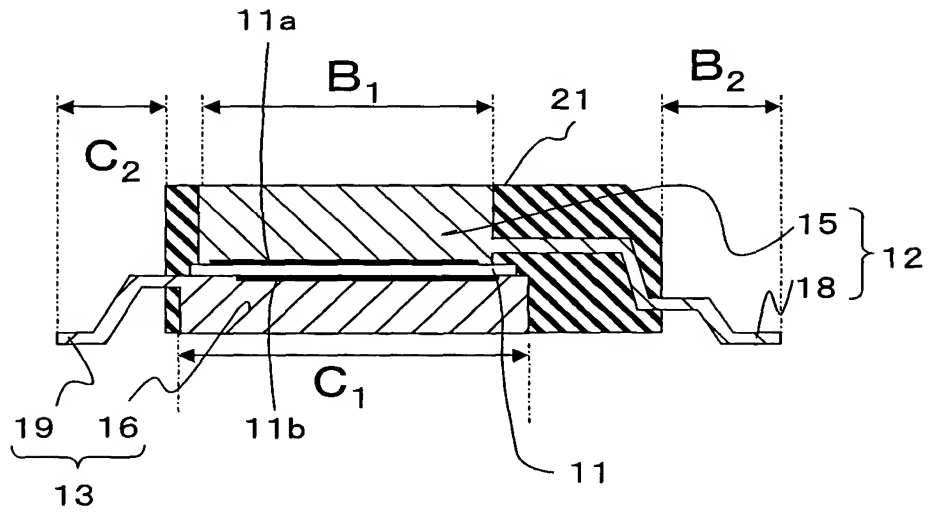


FIG. 3

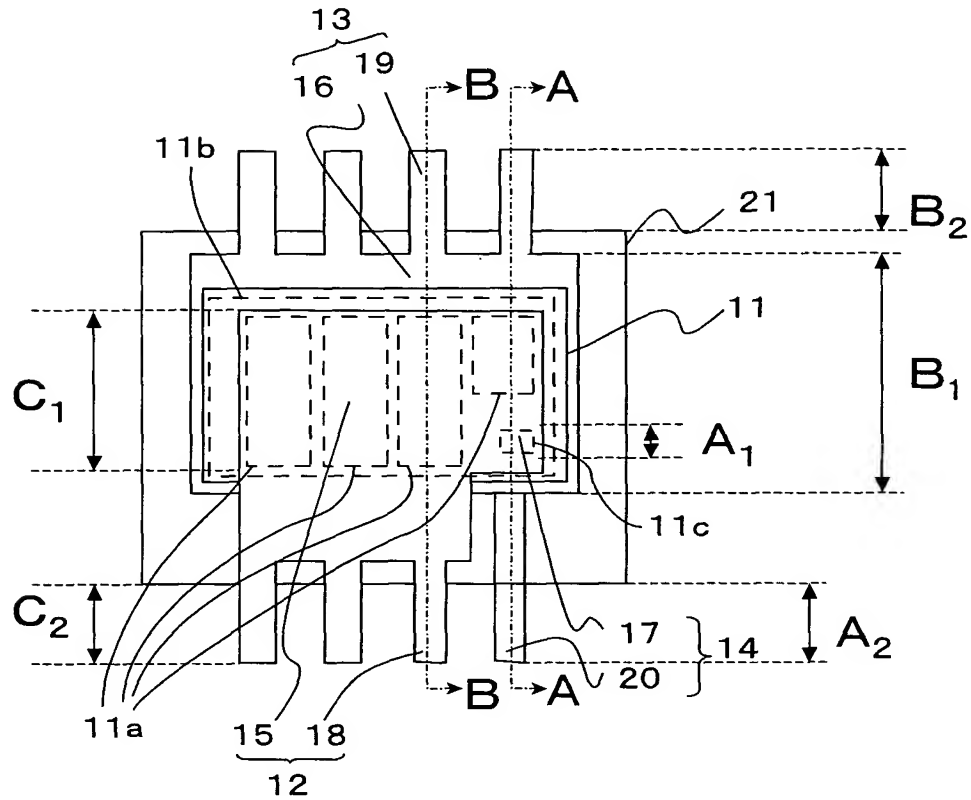


FIG. 4

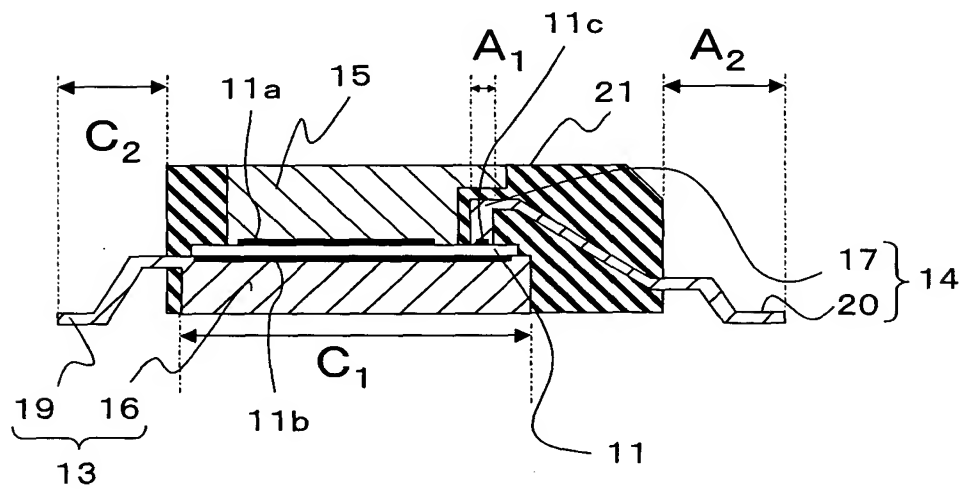


FIG. 5

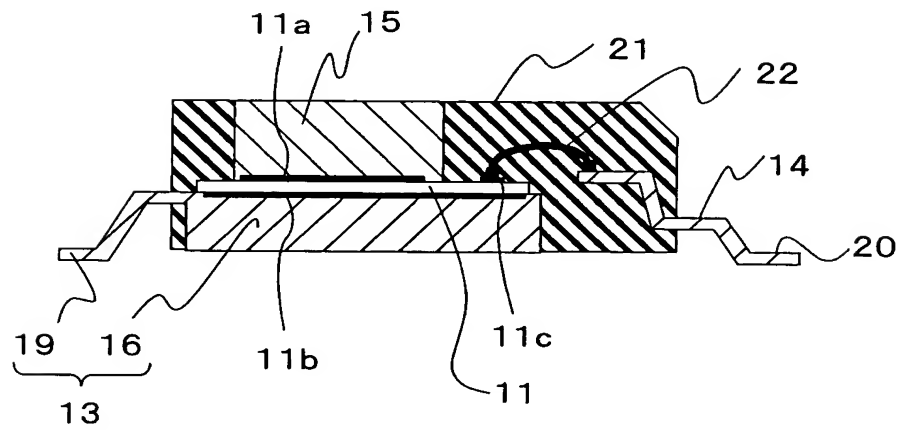


FIG. 6

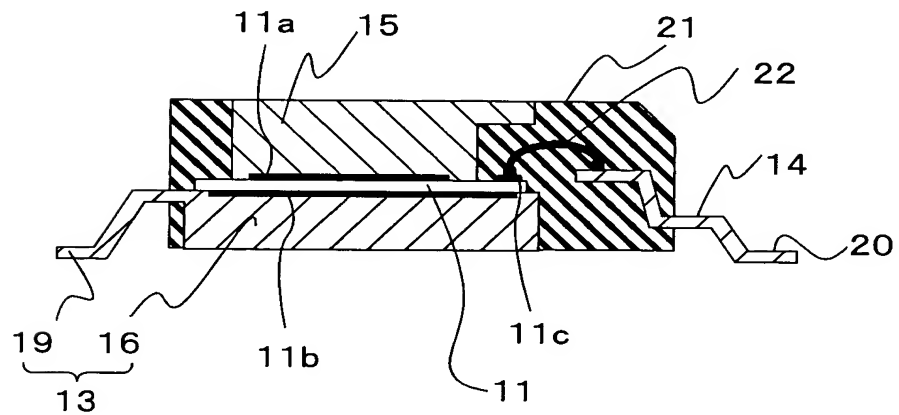


FIG. 7

FIG. 1 is a schematic diagram of a semiconductor device. The device includes a central rectangular region (61) with a dashed border, surrounded by a solid border (61a). This central region is further divided into four quadrants by dashed lines (61b, 61c). The entire structure is enclosed within a larger rectangular frame (61d). The device is connected to a network of conductive lines (62, 63, 64, 65, 66, 67, 68, 69, 70, 71) and a central vertical line (72). Arrows labeled 'A' and 'B' indicate the direction of current flow or signal transmission.

FIG. 9

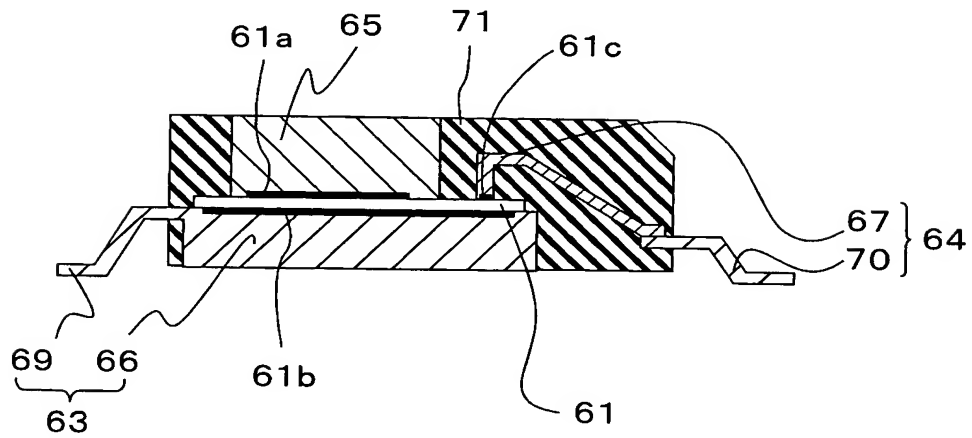


FIG. 10

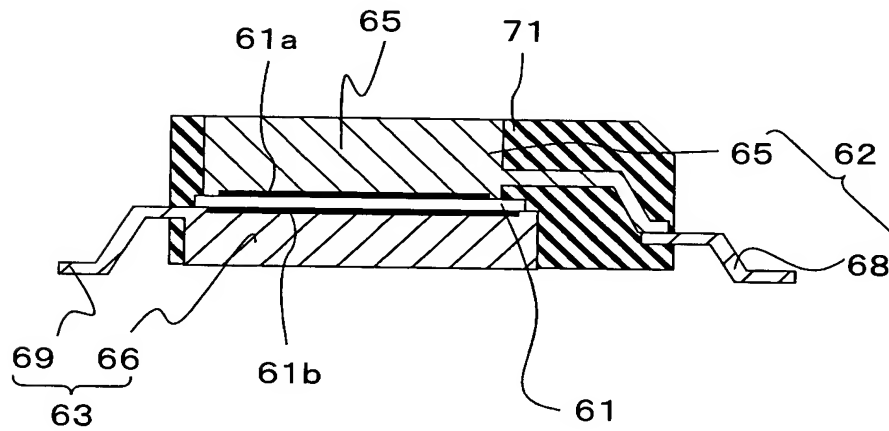


FIG. 11

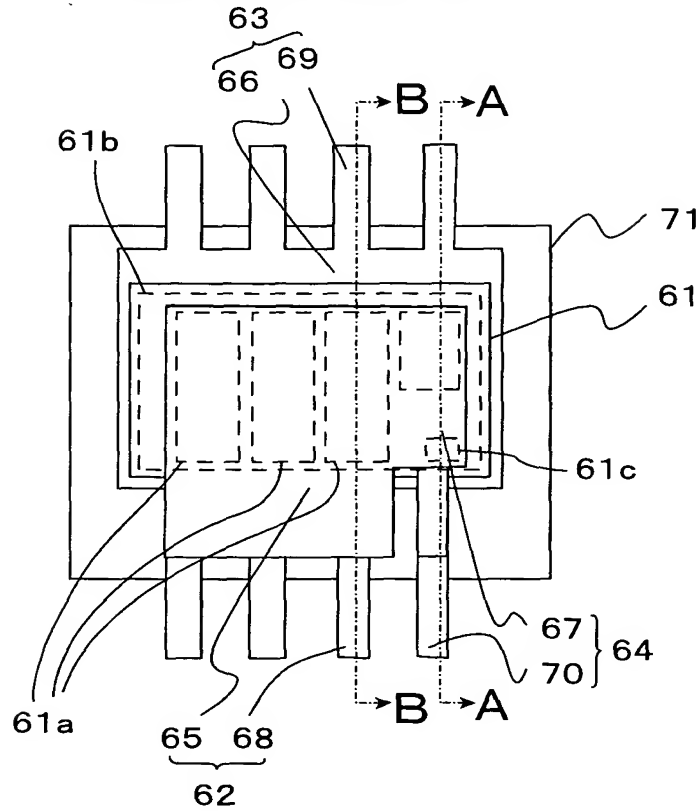


FIG. 12

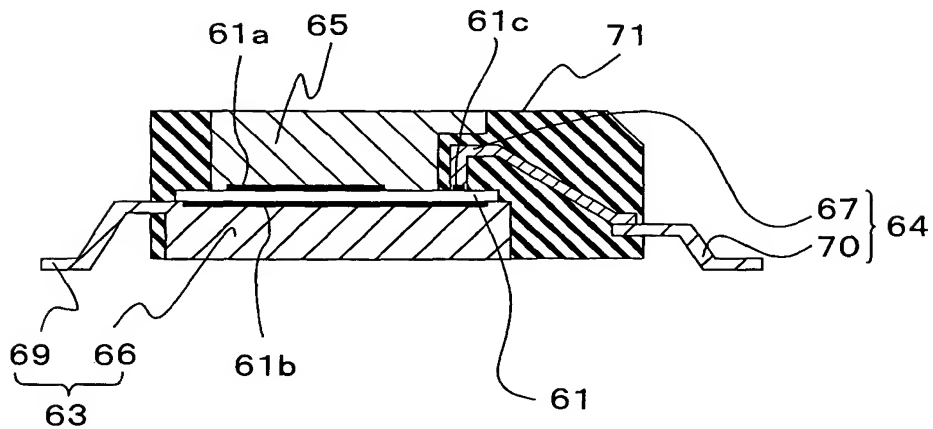


FIG. 13

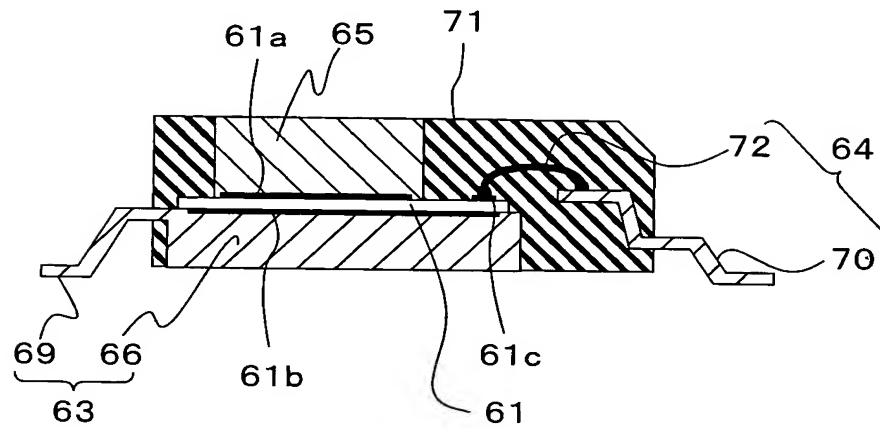


FIG. 14

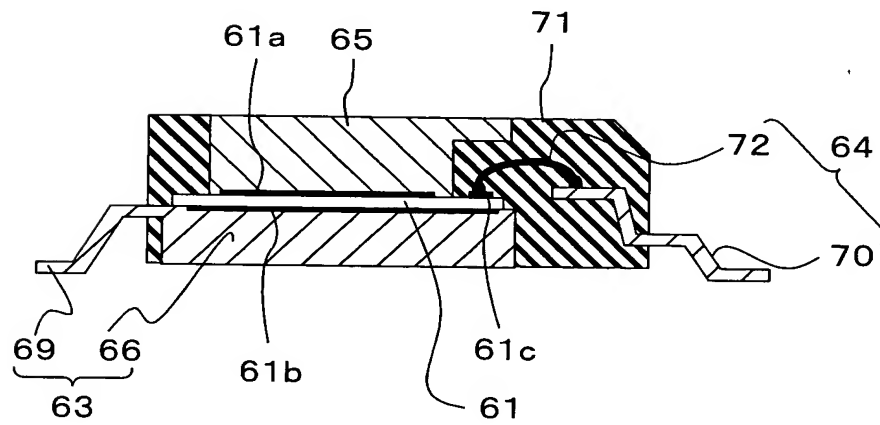


FIG. 15



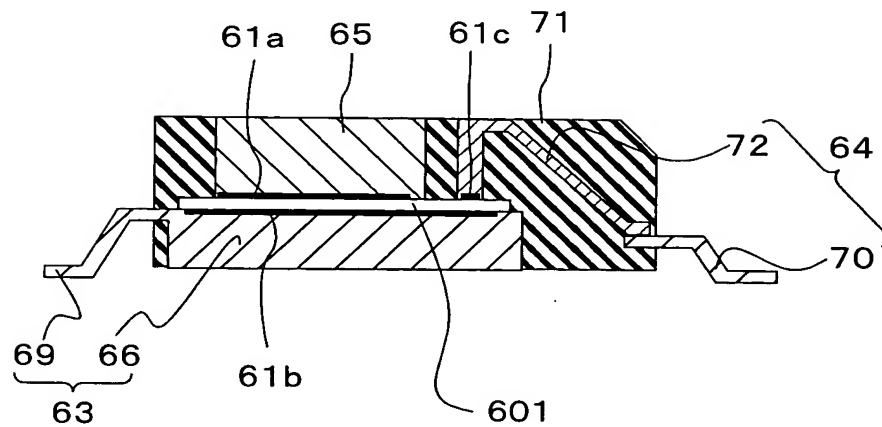


FIG. 16

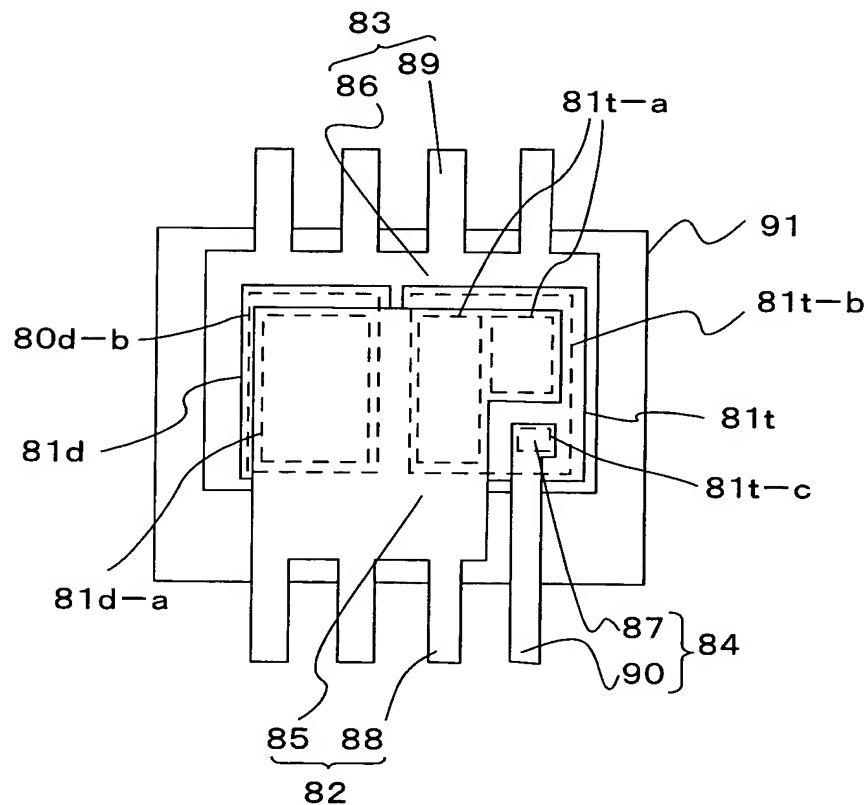


FIG. 17

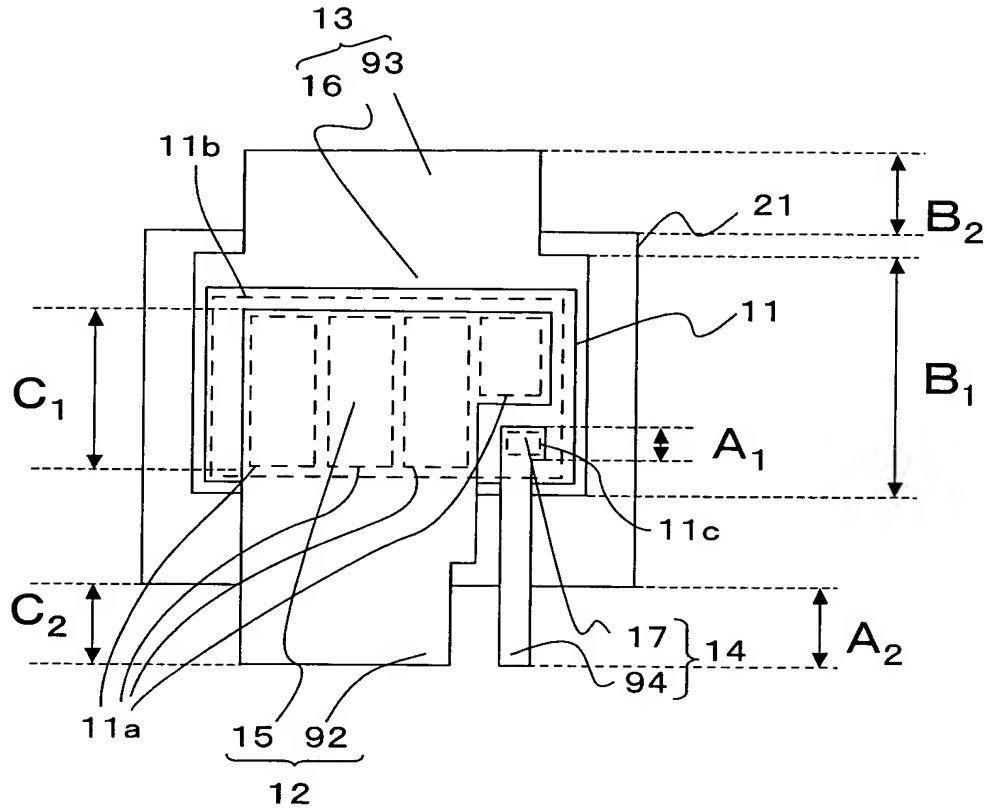


FIG. 18

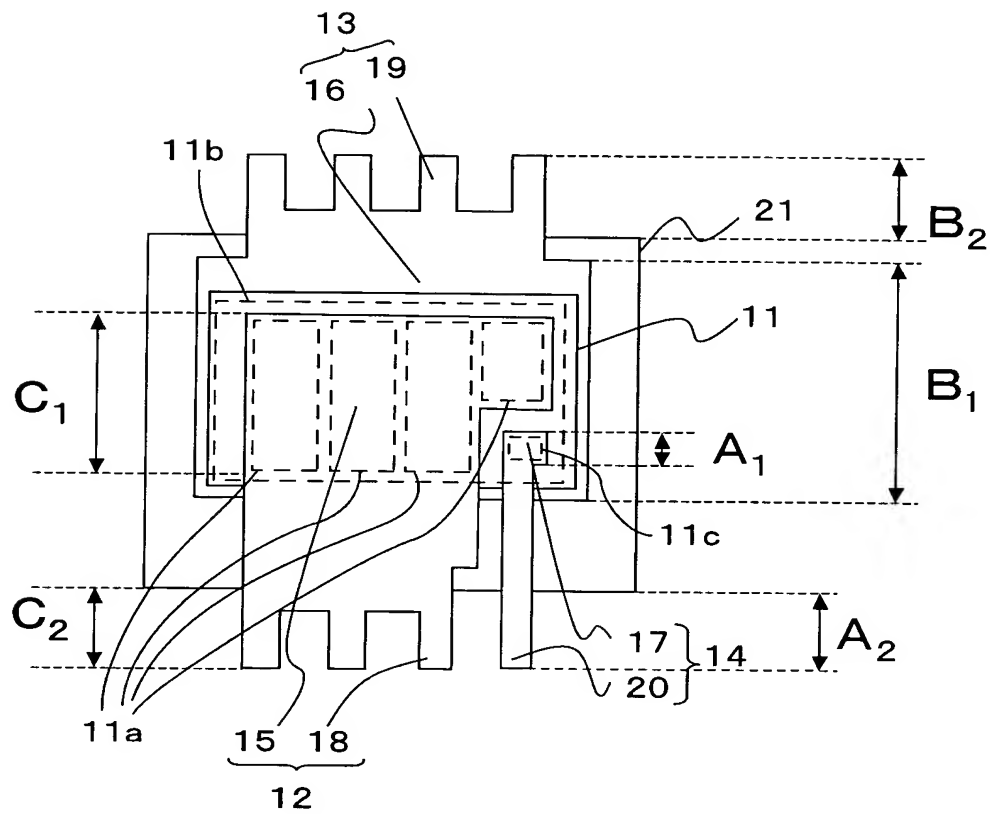


FIG. 19